



## Product Change Notification / SYST-19FKNH955

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### Date:

22-Jun-2020

### Product Category:

8-bit Microcontrollers

### PCN Type:

Document Change

### Notification Subject:

Data Sheet - ATmega4808/4809 Data Sheet

### Affected CPNs:

[SYST-19FKNH955\\_Affected\\_CPN\\_06222020.pdf](#)

[SYST-19FKNH955\\_Affected\\_CPN\\_06222020.csv](#)

### Notification Text:

SYST-19FKNH955

Microchip has released a new Product Documents for the ATmega4808/4809 Data Sheet of devices. If you are using one of these devices please read the document located at [ATmega4808/4809 Data Sheet](#).

**Notification Status:** Final

**Description of Change:** This revision includes the following changes

Chapter	Changes
Entire Document	Removed the content of the Instruction Set Summary section. This section now refers to the external Instruction Set Manual instead.
megaAVR® 0-series Overview	<i>megaAVR</i> ® <i>Device Designations</i> figure updated

Features	48-Pin UQFN replaced by 48-Pin VQFN
Pinout	48-Pin UQFN replaced by 48-Pin VQFN
I/O Multiplexing and Considerations	<i>Multiplexed Signals</i> table updated to replace 48-Pin UQFN with 48-Pin VQFN
Electrical Characteristics	<ul style="list-style-type: none"> <li>• <i>Power Dissipation and Junction Temperature vs. Temperature</i> table replaced UQFN48 with VQFN48 numbers</li> <li>• <i>Power Dissipation and Junction Temperature vs. Temperature</i> table added missing numbers for TQFP48</li> <li>• <i>Power Supply, Reference, and Input Range</i> table added R<sub>INrow</sub></li> <li>• <i>UPDI Max. Bit Rates vs. VDD</i> table corrected VDD condition for 0.9 Mbps</li> </ul>
Ordering information	<ul style="list-style-type: none"> <li>• Removed ordering codes for 48-Pin UQFN</li> <li>• Added ordering codes for 48-Pin VQFN</li> <li>• Added ordering codes for 32-Pin VQFN with we Wettable Flanks</li> <li>• Added ordering codes for 48-Pin VQFN with wettable flanks</li> <li>• <i>Product Identification System</i> figure updated</li> </ul>
Package Drawings	<ul style="list-style-type: none"> <li>• Updated table <i>Drawing Numbers</i> to Microchip editorial standard</li> <li>• Removed 48-Pin UQFN package from <i>Drawing Numbers</i> table</li> <li>• Removed 48-Pin UQFN package drawing</li> <li>• Added 48-Pin VQFN package to <i>Drawing Numbers</i> table</li> <li>• Added 48-Pin VQFN package drawing</li> <li>• Added 32-Pin VQFN package with wettable flanks to <i>Drawing Numbers</i> table</li> <li>• Added 32-Pin VQFN package with wettable flanks drawing</li> <li>• Added 48-Pin VQFN package with wettable flanks to <i>Drawing Numbers</i> table</li> <li>• Added 48-Pin VQFN package with wettable flanks drawing</li> </ul>

**Impacts to Data Sheet:** None

**Reason for Change:** To Improve Manufacturability

**Change Implementation Status:** Complete

**Date Document Changes Effective:** 22 Jun 2020

**NOTE:** Please be advised that this is a change to the document only the product has not been changed.

**Markings to Distinguish Revised from Unrevised Devices:** N/A

## Attachments:

[ATmega4808/4809 Data Sheet](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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Affected Catalog Part Numbers (CPN)

ATMEGA4808-AF  
ATMEGA4808-AFR  
ATMEGA4808-AFR-VAO  
ATMEGA4808-AU  
ATMEGA4808-AUR  
ATMEGA4808-MF  
ATMEGA4808-MFR  
ATMEGA4808-MU  
ATMEGA4808-MUR  
ATMEGA4808-XF  
ATMEGA4808-XFR  
ATMEGA4808-XFR-VAO  
ATMEGA4808-XU  
ATMEGA4808-XUR  
ATMEGA4809-AF  
ATMEGA4809-AF-VAO  
ATMEGA4809-AFR  
ATMEGA4809-AFR-VAO  
ATMEGA4809-AU  
ATMEGA4809-AUR  
ATMEGA4809-MF  
ATMEGA4809-MFR  
ATMEGA4809-MU  
ATMEGA4809-MUR  
ATMEGA4809-PF