

PCN Number:	20171025001	PCN Date:	November 17, 2017
Title:	Datasheet for MSP432P401R, MSP432P401M		
Customer Contact:	PCN Manager	Dept:	Quality Services
Change Type:			
<input type="checkbox"/>	Assembly Site	<input type="checkbox"/>	Design
<input type="checkbox"/>	Assembly Process	<input checked="" type="checkbox"/>	Data Sheet
<input type="checkbox"/>	Assembly Materials	<input type="checkbox"/>	Part number change
<input type="checkbox"/>	Mechanical Specification	<input type="checkbox"/>	Test Site
<input type="checkbox"/>	Packing/Shipping/Labeling	<input type="checkbox"/>	Test Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Site
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Material
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Site
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Materials
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Process

Notification Details

Description of Change:

Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is being updated as summarized below. The following change history provides further details.



MSP432P401R, MSP432P401M

SLAS826G – MARCH 2015 – REVISED SEPTEMBER 2017

Changes from March 8, 2017 to September 26, 2017

Page

• Updated Section 1.3, Description	2
• Changed the name of the ADC module from ADC14 to Precision ADC throughout document.....	3
• Added VSW to Table 4-4, Connection for Unused Pins	27
• Removed MAX values from Section 5.21 and Section 5.22	39
• Added Section 5.25.2, Peripheral Register Access Timing	41
• Changed the MAX value of the df_{DCO}/dT parameter in external resistor mode to 60 ppm/°C in Table 5-12, DCO	51
• Changed the MAX value of the f_{VLO} parameter to 18 kHz in Table 5-14, Internal Very-Low-Power Low-Frequency Oscillator (VLO)	53
• Changed the MAX value of the "REFO absolute tolerance calibrated" parameter at $T_A = -40^\circ\text{C}$ to 85°C to $\pm 4\%$ in Table 5-15, Internal-Reference Low-Frequency Oscillator (REFO) in 32.768-kHz Mode	53
• Changed the TYP and MAX values of the TC_{REF+} parameter in Table 5-34, REF_A, Built-In Reference	77
• Removed interrupt-related registers from Port 7 to Port 10 in Table 6-21, Port Registers (Base Address: 0x4000_4C00)	101
• Corrected the bank 0 address range (changed 0x2000_007FF to 0x2000_07FF) in the last paragraph in Section 6.4.2.2, SRAM Bank Retention Configuration and Backup Memory	114
• Clarified DMA access to flash banks in Section 6.5.3, DMA Access Privileges	115
• Corrected the mnemonics for the timer functions in Table 6-44 and Table 6-45	125
• Corrected the ADC channel mapping for the Battery Monitor and Temperature Sensor in Table 6-53, Precision ADC Channel Mapping on 80-Pin ZXH Devices , and Table 6-54, Precision ADC Channel Mapping on 64-Pin RGC Devices	133

The datasheet number will be changing.

Device Family	Change From:	Change To:
MSP432P401R, MSP432P401M	SLAS826F	SLAS826G

These changes may be reviewed at the datasheet links provided.

<http://www.ti.com/product/MSP432P401R>

Reason for Change:

Improve the device's description and more accurately reflect device characteristics.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.

Changes to product identification resulting from this PCN:

None.

Product Affected:			
MSP432P401MIPZ	MSP432P401MIPZR	MSP432P401MIRGCR	MSP432P401MIRGCT
MSP432P401MIZXHR	MSP432P401MIZXHT	MSP432P401RIPZ	MSP432P401RIPZR
MSP432P401RIRGCR	MSP432P401RIRGCT	MSP432P401RIZXHR	MSP432P401RIZXHT

For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

Location	E-Mail
USA	PCNAmericasContact@list.ti.com
Europe	PCNEuropeContact@list.ti.com
Asia Pacific	PCNAsiaContact@list.ti.com
Japan	PCNJapanContact@list.ti.com